

NEWS RELEASE

FOR: ESC ELECTRONICS CORP.
PALISADES PARK, NEW JERSEY

TWO NEW MINIATURE IFF TRANSPONDER DELAY LINES

MOUNT ON PRINTED CIRCUIT BOARD

The newest IFF transponder delay lines developed by ESC are distinguished by their relatively miniature size and construction. Models 53-89 and 53-92 exhibiting delays of 20.3 microseconds and 24.65 microseconds respectively each occupy only 4" x 4" x 3/8" of printed circuit board space and can be supplied as separate PC mounting components or together with associated circuitry.

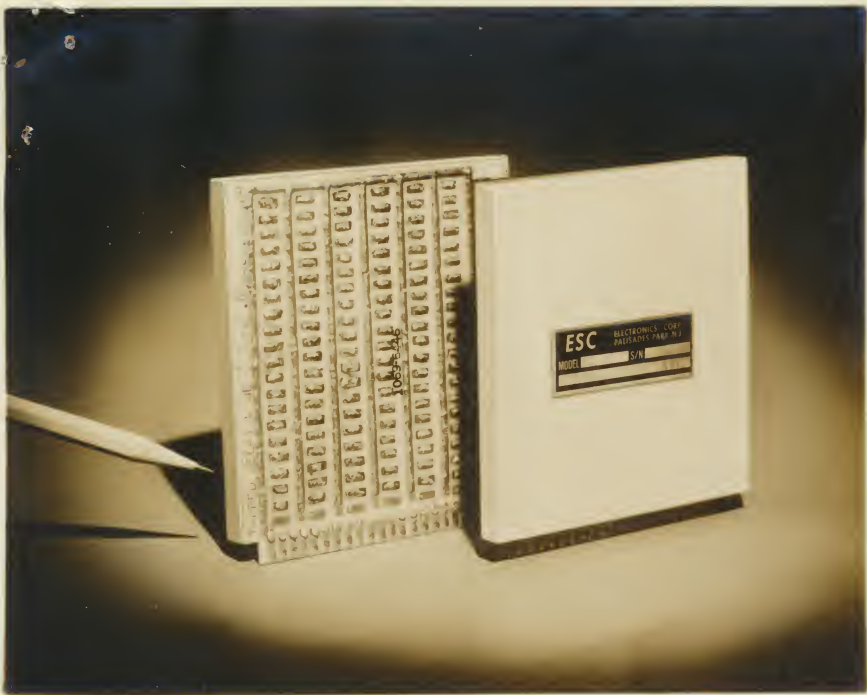
Impedance is 400 ohms for the Model 53-89 and 470 ohms for the Model 53-92. The delay to rise time ratio is better than 50:1, attenuation is less than 0.12 db/microsecond and temperature coefficient less than 50 ppm/°C. over a temperature range of -55°C to +85°C.

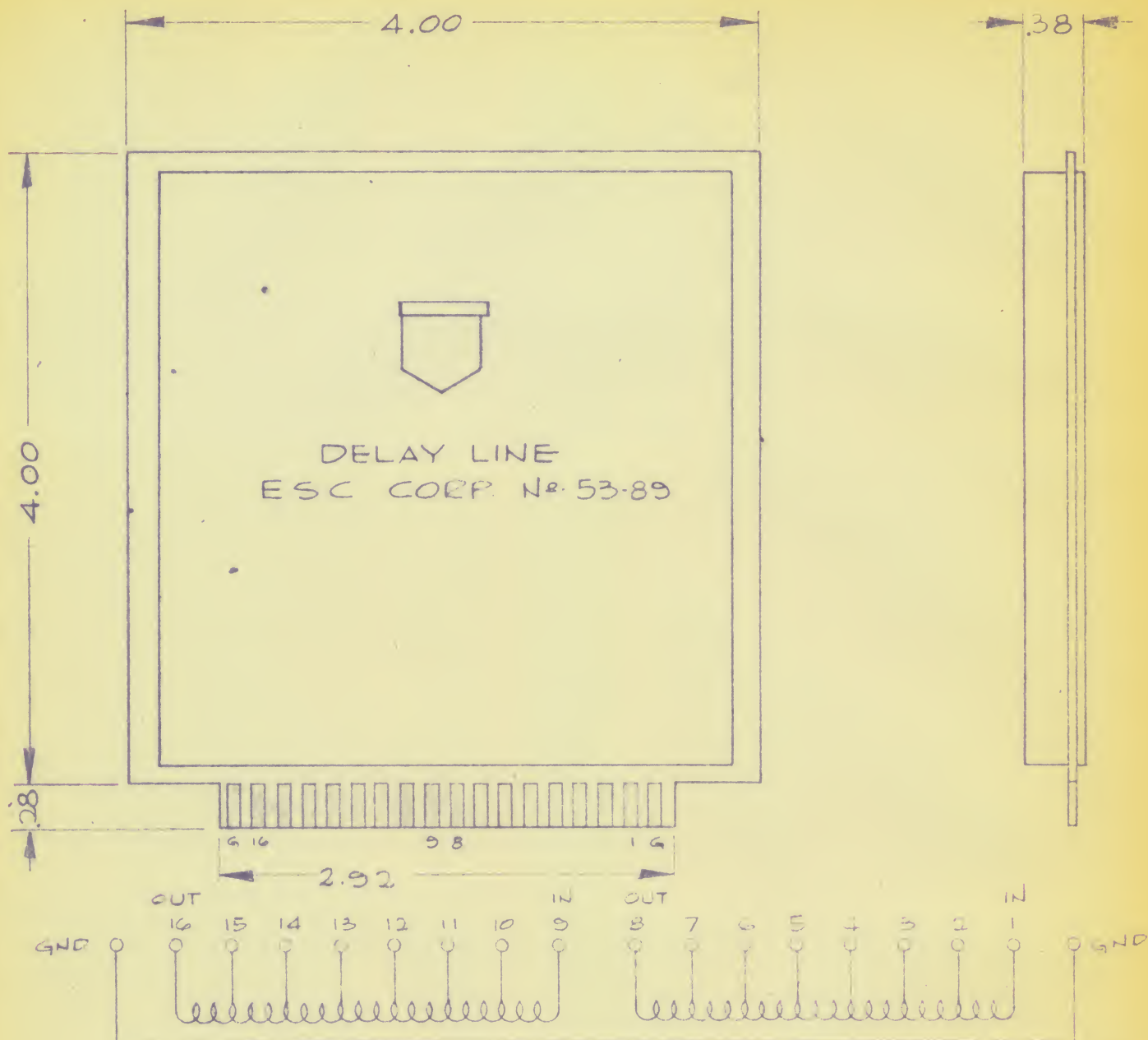
Taps are provided at 1.45 microsecond intervals to a ±.05 microsecond tolerance, but can be relocated as required. Units are available for short term test purposes on a consignment and "first come first served" basis.

For more information contact ESC Electronics Corp., 534 Bergen Boulevard, Palisades Park, New Jersey, or telephone (201) 947-0400.

#

AR 12/11/64 ESC





2	30.64	REVISED TEMP. RANGE
1	10.16.64	ADDED DIAGRAM

REV.	DATE	DESCRIPTION
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TOLERANCES:
 2 PLACE DEC. = $\pm .030$
 3 PLACE DEC. = $\pm .010$

MATERIAL

FINISH Printed Circuit Board
 Encapsulated in Silicone Rubber

ISSUED
 10.2.64

USED WITH

OWN
 DP

APP'D

ESC

534 BERGEN BOULEVARD
 PALISADES PARK, N. J.

TITLE

DELAY LINE

Sheet 1

DWG. NO.

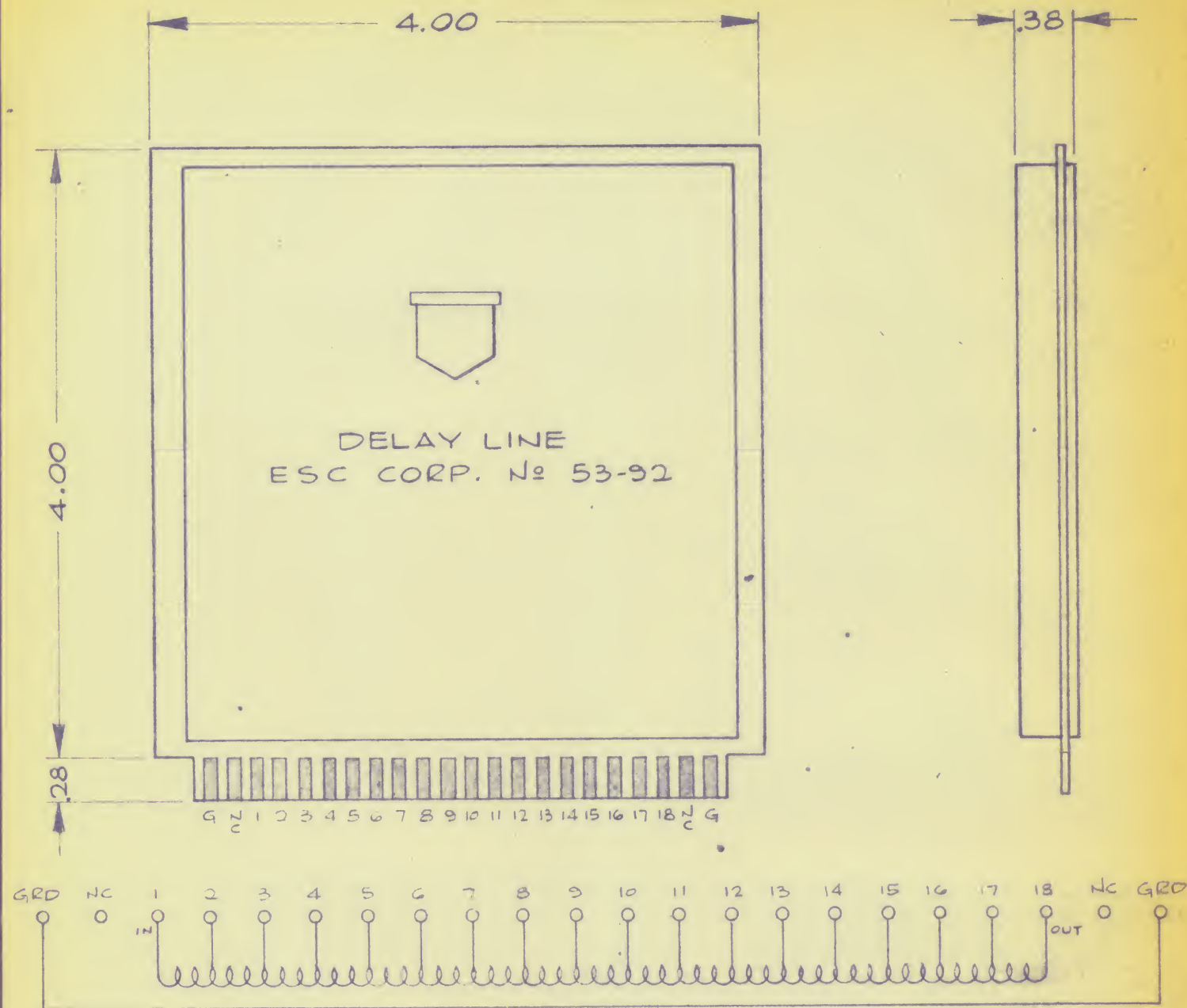
53-89

UNLESS OTHERWISE SPECIFIED

CHARACTERISTICS

Delay Time	20.3 usec. \pm .1 usec.
Taps	every 1.45 usec. \pm .05 usec. of nominal time delay
Terminating Resistance	400 Ω \pm 1%
Input Resistance	375 Ω \pm 1%
Characteristic Impedance	400 Ω \pm 5%
Output Rise Time	.40 usec. max.
with Input Rise Time	.02 usec. max.
Distortion	10 % max.
with Input Rise Time	.02 usec.
Attenuation	2.4 db at 25°C
Temperature Coefficient	50 ppm/°C max.
Temperature Range	-55°C to +105°C

TOLERANCES 2 PLACE DEC. = \pm .030 3 PLACE DEC. = \pm .010 UNLESS OTHERWISE SPECIFIED		MATERIAL Printed Circuit Board Encapsulated in Silicone Rubber		REV. DATE DESCRIPTION	
		FINISH		ESC 534 BERGEN BOULEVARD PALISADES PARK, N. J.	
		ISSUED 10.6.64		TITLE Sheet 2 of 2	
		USED WITH		DWG. NO. 53-89	
		OWN DN			



1	11 30 64	REVISED TEMP. RANGE
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REV.	DATE	DESCRIPTION
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TOLERANCES:
2 PLACE DEC. = $\pm .030$
3 PLACE DEC. = $\pm .010$

MATERIAL

FINISH Printed Circuit Board
Encapsulated in Silicone Rubber

ISSUED

USED WITH

DWN

APP'D

11.12.64

SSP

ESC

534 BERGEN BOULEVARD
PALISADES PARK, N. J.

TITLE

DELAY LINE

Sheet 1

DWG. NO.

53-92

UNLESS OTHERWISE SPECIFIED

CHARACTERISTICS

Delay Time	24.65 usec. \pm .1 usec.
Taps	every 1.45 usec. \pm .05 usec. of nominal time delay
Terminating Resistance	470 Ω \pm 1%
Input Resistance	445 Ω \pm 1%
Characteristic Impedance	470 Ω \pm 5%
Output Rise Time	0.47 usec. max.
with Input Rise Time	.02 usec. max.
Distortion	10% max.
with Input Rise Time	.02 usec.
Attenuation	2.5 db at 25°C
Temperature Coefficient	50 ppm/°C max.
Temperature Range	- 55°C to + 105°C

					REV.	DATE	DESCRIPTION
TOLERANCES: 2 PLACE DEC. = \pm .030 3 PLACE DEC. = \pm .010 UNLESS OTHERWISE SPECIFIED		MATERIAL Printed Circuit Board Encapsulated in Silicone Rubber			ESC, 534 BERGEN BOULEVARD PALISADES PARK, N. J.		
		FINISH			TITLE		
					Sheet 2 of 2		
		ISSUED 11.12.64	USED WITH	DWN SSP	APP'D	DWG. NO. 53-92	

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In response to your recent request, we are happy to send our latest technical bulletin.

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ESC CORPORATION

534 Bergen Boulevard
Palisades Park, N. J.

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Lines Catalog.

NAME _____ POSITION _____

COMPANY NAME _____

COMPANY ADDRESS _____







ESC

ELECTRONICS CORP.

534 BERGEN BOULEVARD • PALISADES PARK, N. J.

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